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Drexel VLSI and Architecture Laboratory (Drexel University. Department of Electrical and Computer Engineering. 3141 Chestnut Street (map)

VLSI & Embedded System Lab -

Lab location: 3rd Floor, Room No. 310 Area of the lab in carpet: 100.37 sq. mt. Cost of the equipments in the lab: Rs. 50,60,342/-Name of the Lab Incharge: Prof

Low-Power VLSI Laboratory - Purdue University -

The laboratory is located in the Department of Electrical & Computer Engineering at the West Lafayette campus. noise aware circuit design and VLSI testing.

VLSI Lab - NTU EEE -

Location: S2.1-B3-01. Telephone: 6790-6511 Laboratory Staff. SEOW Yong Hing, William SIA Liang Poo This laboratory supports teaching and research in System Design

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VLSI Laboratory (VLSI_ Lab) - Research Group | -

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